

Lista a maximum 10 publicații relevante

S.L. Dr. Ing. Cătălin Bogdan CIOBANU

1. **C.B. Ciobanu**, Introducere în Proiectarea Sistemelor Încorporate, Editura Universității Transilvania din Brașov, 2022, ISBN 978-606-19-1564-4
2. **C.B. Ciobanu**, G. Gaydadjiev, C. Pilato and D. Sciuto, The Case for Polymorphic Registers in Dataflow Computing, International Journal of Parallel Programming, December 2018, pp. 1185-1219, <https://doi.org/10.1007/s10766-017-0494-1>

Online:

<https://link.springer.com/article/10.1007/s10766-017-0494-1>

Impact Factor: 1.258

3. A. L. Machidon, O. M. Machidon, **C. B. Ciobanu**, and P. L. Ogrutan, "Accelerating a Geometrical Approximated PCA Algorithm Using AVX2 and CUDA," Remote Sensing, vol. 12, no. 12, p. 1918, Jun. 2020
<https://doi.org/10.3390/rs12121918>

Online:

<https://www.mdpi.com/2072-4292/12/12/1918/htm>

Impact Factor 4.848, Q1

4. Ramirez, F. Cabarcas, B.H.H. Juurlink, M. Alvarez, F. Sanchez, A. Azevedo, C.H. Meenderinck, **C.B. Ciobanu**, S. Isaza, G. N. Gaydadjiev, The SARC Architecture, IEEE Micro, pp. 16-29, Vol. 30, Nr. 5, ISSN: 0272-1732,
<https://doi.org/10.1109/MM.2010.79>

Online:

<https://ieeexplore.ieee.org/document/5567090>

Impact Factor: 2.8, Q2

5. **C. B. Ciobanu**, G. Stramondo, C. de Laat and A. L. Varbanescu, MAX-PolyMem: High-Bandwidth Polymorphic Parallel Memories for DFEs, 2018 IEEE International Parallel and Distributed Processing Symposium Workshops (IPDPSW), pp. 107-114, Vancouver, Canada, May 2018, **Best paper award**, **Indexată ISI**, <https://doi.org/10.1109/IPDPSW.2018.00025>
6. **C.B. Ciobanu**, G. N. Gaydadjiev, Separable 2D Convolution with Polymorphic Register Files, Proceedings of the 2013 Conference on Architecture of Computing Systems (ARCS 2013), pp. 317-328, Prague, Czech Republic, February 2013, **Indexată ISI**, https://doi.org/10.1007/978-3-642-36424-2_27
7. **C.B. Ciobanu**, G. Kuzmanov, G. N. Gaydadjiev, On Implementability of Polymorphic Register Files, Proceedings of the 7th International Workshop on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC 2012), pp. 1-6, York, UK, July 2012, **Indexată ISI**, <https://doi.org/10.1109/ReCoSoC.2012.6322873>
8. A. Pușcașu, **C. B. Ciobanu** and O. Buiu, "Systolic Array Matrix Multiplication Accelerator," 2024 International Semiconductor Conference (CAS), Sinaia, Romania, 2024, pp. 207-210, **Best student paper award**, doi: <https://doi.org/10.1109/CAS62834.2024.10736842>
9. **C.B. Ciobanu**, D. Manolescu, R. Vladuta, L. Chitu, C. Moisa, M. Manofu, P. Svasta, "TIE Micro – Chiplets and Next-gen Packaging," 2024 IEEE 30th International Symposium for Design and Technology in Electronic Packaging (SIITME), Sibiu, Romania, 2024, pp. 504-511, **Best paper award**, <https://doi.org/10.1109/SIITME63973.2024.10814902>
10. **C.B. Ciobanu**, H. Gâlmeanu, A. Pușcașu, M. Gologanu, O. Buiu, M. Antonescu, V.G. Serbu, V.M. Moise, C.T. Axinte, A.T. Popovici, G.I. Uleru, A. Stan, M. Munteanu, A. Drîmbărean, C. Nemeti, D. Rotar, C. Moisa, B. Ditu, P.C. Trusca, M. Antache, S.

Costinescu, M.A. Sachian, G. Suci, C. Gheorghe, C. Tudor, K. Koci, "RISC-V Accelerators, Enablement and Applications for Automotive and Smart Home in the ISOLDE Project", in Proc. Int. Conf. on Embedded Computer Systems: Architectures, Modeling and Simulation (SAMOS) 2024, pp 1-16, Samos, Greece, July 2024

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